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STATEMENT BY APPLICANT**

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Sheet	1	of	2
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Complete if Known

Application Number	10/017,669
Filing Date	October 30, 2001
First Named Inventor	Youfeng Wu
Group Art Unit	2122
Examiner Name	Unassigned
Attorney Docket Number	42390P10350

U.S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

[illegible]

Examiner Signature	<i>Norman Antonio Duran</i>	Date Considered	9/3/04
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*Unique citation designation number. *See attached Kinds of U.S. Patent Documents. *Enter Office that issued the document, by the two-letter code (WIPO Standard S.3). *For Japanese patent documents, the indication of the year of reign of the Emperor must precede the serial number of the patent document. *Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. *Applicant is to place a check mark here if English language Translation is attached.

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Substitute for form 1449A/PTO				Complete if Known	
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				Examiner Name	Unassigned
Sheet	2	of	2	Attorney Docket Number	42390P10350

OTHER ART - NON PATENT LITERATURE DOCUMENTS		
Examiner Initials*	Cite No.†	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item, book, magazine, journal, serial, symposium, catalog, etc., date, page(s), volume-issue number(s), publisher, city and country where published.
<i>Ham</i>	C	PATH PROFILE GUIDED PARTIAL DEAD CODE ELIMINATION USING PREDICATION, by RAJIV GUPTA, et al., PACT '97, November 11-15, 1997 in San Francisco, California
	D	COMPILER-DIRECTED DYNAMIC COMPUTATION REUSE: RATIONALE AND INITIAL RESULTS, by DANIEL A. CONNORS, et al., Department of Electrical and Computer Engineering Center for Reliable And High-Performance Computing, University of Illinois, Urbana, IL 61801, Nov. 1999
	E	IMPROVING TRACE CACHE EFFECTIVENESS WITH BRANCH PROMOTION AND TRACE PACKING, by SANJAY JERAM PATEL, et al., Advanced Computer Architecture Laboratory, Department of Electrical Engineering and Computer Science, The University of Michigan, Ann Arbor, Michigan 48109-2122
	F	THE SUPERBLOCK: AN EFFECTIVE TECHNIQUE FOR VLIW AND SUPERSCALAR COMPILATION, by WEN-MEI W. HWU, et al., Center for Reliable and High-Performance Computing, University of Illinois, Urbana-Champaign, Illinois, 61801.
<i>Y</i>	G	REPLAY: A HARDWARE FRAMEWORK FOR DYNAMIC PROGRAM OPTIMIZATION, by SANJAY J. PATEL, et al., Center for Reliable and High-Performance Computing, The University of Illinois at Urbana-Champaign, CRHC Technical Report # CRHC-99-16, December 1999

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Examiner Signature	<i>Horangun Antonio Reyes</i>	Date Considered	9/3/04
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